

Workshop on System on a Chip (SOC) and Computer

Architecture

with CFP of 系統晶片與計算機結構

Call for papers

Aims and scope

2017 workshop on System on a Chip (SOC) and Computer Architecture is to provide a forum for researchers, educators, industrials and professionals to exchange their updated contributions covering those aspects about System on a Chip (SOC) and Computer Architecture. The 2017 National Computer Symposium (NCS 2017) will be held from December 14-15, 2017 AT National Dong Hwa University (國立東華大學) TAIWAN, (NCS WEB-PAGE AT <http://ncs2017.ndhu.edu.tw/bin/home.php>).

Topic of Interests

Areas of interest include, but are not limited to, the following topics:

- Analog, mixed-signal and sensor architectures
- Digital architectures: NoC, multi-core, and reconfigurable
- CAD: Synthesis and analysis
- Prototyping, verification, modeling, and simulation
- Circuits and systems for signal processing and communications
- Embedded systems: Architecture, design, and software
- Low-power and thermal-aware IC design
- Emerging semiconductor technologies
- Variability, reliability, and test
- Hardware security
- Arduino and Raspberry Pi-based systems and related applications
- Application-specific systems
- Benchmarking, performance measurements, and analysis
- Cloud, Grid, cluster, and peer-to-peer systems
- Embedded and pervasive systems
- GPUs, FPGAs and other accelerator architectures
- High-performance system design for analytics and social networking
- Languages, compilers, and tools for parallel and distributed programming
- Modeling and simulation methodology

- Operating systems and virtualization
- Parallel and distributed systems, algorithms, and applications
- Power and energy-efficient systems
- Processor, cache, memory, storage, and network architecture
- Real-world applications and case studies
- Reconfigurable and fault-tolerant systems
- Data mining, video surveillance system

Journal Publication Recommendations

1. JOC-Journal of Computers (EI)
2. Journal of Chung Cheng Institute of Technology (中正嶺學報)(EI)

Important Dates

Paper submission deadline: OCT. 25, 2017

Notification of Acceptance: NOV. 02-08, 2017

Final Camera Ready Paper Submission: NOV. 10-19, 2017

Conference: DEC. 14-15, 2017

Workshop Organizing Committees

Chair: Dr. Tso-Bing Juang/莊作彬 (國立屏東大學 資訊工程學系)

Vice-Chair: Dr. Chao-Tsung Kuo/郭昭宗 (國立金門大學 電子工程學系)

Program Committees

Dr. Ming-Chih Chen/陳銘志 (國立高雄第一科技大學 電子工程系)

Dr. Chun-Chi Chen/陳俊吉(國立高雄第一科技大學 電子工程系)

Dr. Hung-Yu Wang/王鴻猷 (國立高雄應用科技大學 電子工程系)

Dr. (Shu-Chung Yi)/易昶霽 (國立彰化師範大學 資訊工程學系)

Dr. Liang-Cheng Shiu/許良政 (國立屏東大學 資訊工程學系)

Dr. Kun-Chih (Jimmy) Chen/陳坤志 (國立中山大學 資訊工程學系)