

National Computer Symposium 2017 (2017 全國計算機會議)

Workshop on Image Processing (影像處理)

Call for Papers

Goal of the Workshop:

2017 Workshop on Image Processing will be a forum for researchers, educators, industrials and professionals to exchange their updated contributions, latest results and work-in-progress in the area. The workshop also explores future trends and applications in these areas and other related areas. Distinguished papers of this workshop will recommend to special issues of SCI and EI journals.

About the Workshop:

The Workshop on Image Processing in conjunction with NCS 2017 will be held in Hualien, National Dong Hwa University (國立東華大學), TAIWAN, December 14-15, 2017. All relevant dates, location, and travel information are available from the NCS 2017 conference site: <http://ncs2017.ndhu.edu.tw/>

Scope of the Workshop:

Topics of interest include all aspects of image processing, but not limited to:

- Deep Learning for Image Processing
- Artificial Intelligence and Machine Learning for Image Processing
- Image/Video Content Analysis and Understanding
- Object Detection and Recognition
- Computer Vision Algorithms and Technologies
- Restoration, Enhancement, and Super-Resolution
- Compression, Transmission, Storage, and Retrieval
- Computational Imaging
- 3D Imaging with Applications
- Perceptual-based Image Processing
- Visual Quality Assessment
- Hyperspectral Image Processing
- Other Image Processing Applications

Important Dates:

Paper submission deadline: Oct. 25, 2017

Notification of Acceptance: Nov. 02-08, 2017

Camera-Ready Paper Submission: Nov. 10-19, 2017

Conference Data: Dec. 14-15, 2017

Paper Submission Guidelines:

Before submission, authors should carefully read over the submission guidelines. All papers must be submitted in an electronic format according to the workshop proceedings style. All relevant paper format, requirements, and date are available from the NCS 2017 conference site: <http://ncs2017.ndhu.edu.tw/>

Workshop Organizing Committees:

Chair: Prof. Chia-Hung Yeh/葉家宏 (國立臺灣師範大學 電機工程學系)

Co-Chairs: Prof. Chuan-Yu Chang/張傳育 (國立雲林科技大學 資訊工程學系)

Co-Chairs: Prof. Li-Wei Kang/康立威 (國立雲林科技大學 資訊工程學系)

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Prof. Chuan-Yu Chang/張傳育 (國立雲林科技大學 資訊工程學系)

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Prof. Chia-Yen Chen/陳佳妍 (Department of Computer Science, University of Auckland)